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## APPLICANTS

Seiichiro Sasaki, Tokyo, JAPAN;  
 Kouji Morita, Saitama, JAPAN;

\*\* CONTINUING DATA *None* *Yes*

\*\* FOREIGN APPLICATIONS *Yes*  
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## IF REQUIRED, FOREIGN FILING LICENSE GRANTED

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Foreign Priority claimed <input checked="" type="checkbox"/> yes <input type="checkbox"/> no	STATE OR COUNTRY JAPAN	SHEETS DRAWING 5	TOTAL CLAIMS 15	INDEPENDENT CLAIMS 3
35 USC 119 (a-d) conditions <input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> Met after met Verifier and Acknowledged <i>Yoshi Sasaki</i> <i>Initials</i> <i>VS</i>	Allowance Examiner's Signature Initials			

## ADDRESS

RABIN & BERDO, P.C.  
 Suite 500  
 1101 14th Street  
 Washington, DC20005

## TITLE

Multilayered power supply line for semiconductor integrated circuit and layout method thereof

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